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- 1** A parallel algorithm for record clustering 100%

Edward Omiecinski , Peter Scheuermann

ACM Transactions on Database Systems (TODS) December 1990

Volume 15 Issue 4

We present an efficient heuristic algorithm for record clustering that can run on a SIMD machine. We introduce the P-tree, and its associated numbering scheme, which in the split phase allows each processor independently to compute the unique cluster number of a record satisfying an arbitrary query. We show that by restricting ourselves in the merge phase to combining only sibling clusters, we obtain a parallel algorithm whose speedup ratio is optimal in the number of processors used. Final ...
- 2** Pulsa: non-blocking packet switching with shift-register rings 100%

G. J. Murakami , R. H. Campbell , M. Faiman

ACM SIGCOMM Computer Communication Review , Proceedings of the ACM symposium on Communications architectures & protocols August 1990

Volume 20 Issue 4

This paper discusses the design of a switch for high-speed computer networking at gigabit rates: We present the Pulsar switch, a non-blocking design based on a high-spin-rate, port-dedicated, word-parallel, shift-register ring. Several design alternatives address the problem of Head-Of-Line blocking. In contrast to Batcher-Banyan switches, access to the ring is asynchronous which facilitates low delay and arbitrary packet length. The switch can support ATM cells simultaneou ...
- 3** The Mesa programming environment 100%

Richard E. Sweet

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 85 symposium on